Art Unit:



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : H. Zhang Serial No.: 09/362,808

Examiner:

2814 Thanh V.

Filed : July 28, 1999

Title : METHOD OF FABRICATING SEMICONDUCTOR DEVICE

Commissioner for Patents Washington, D.C. 20231

TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims 40

37

\$54

Independent

6

3 0

\$0

TOTAL FEE DUE

\$54

A check for \$54 is attached.

Respectfully submitted,

Harris₄ Reg. No. 32,030

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Susan Regan

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Applicant : H. Zhang

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AMENDMENT

Sir:

In response to the Official Action mailed November 5, 2001, Paper No. 9, in the above-referenced application, please amend the above-identified application as follows.

In the Claims:

Please amend claims 1, 2, 10, 14, 19, and 24 as follows:

1. (Amended) A semiconductor device comprising:

a semiconductor having at least channel, source and drain regions;

an insulating film formed on said semiconductor;

a first interlayer insulating film over said insulating film;

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| February: | 5, 200 |)2 |
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